

IN THE SPECIFICATION:

Please add the following paragraph starting at page 1, line 1.

-- This application is a divisional of U.S. Patent Application No. 08/307,494, filed September 16, 1994, now U.S. Patent No. 6,298,388 B1, which is a continuation of U.S. Patent Application No. 07/803,608, filed December 9, 1991, now abandoned.--

Please amend the paragraph starting at page 3, line 11 as follows.

--In recent years, the realization of miniaturization of the apparatus is more and more progressed. In the apparatus as mentioned above, it is becoming impossible to provide a space enough to attach both of the connector 60 for the IC memory and the external expansion bus connector 89. For instance, there are also commercially available a portable data processing apparatus and the like such as IC card system pocket book, IC card electronic calculator, and the like having a small space only for providing a connector terminal for the IC card.--

Please amend the paragraph starting at page 3, line 27 as follows.

--As mentioned above, however, in recent years, it is difficult to assure an installation space of the connector to input/output for an external device. In the conventional construction, there is a case of using a countermeasure such that the control of the external device and data input/output operation are executed or the like by using a serial communication connector. This is because an installation space for the serial communication connector is smaller than that for the external expansion bus connector since the serial communication connector has only a signal line.--

Please amend the paragraph starting at page 8, line 1 as follows.

--In a manner similar to the conventional apparatus, internal circuits of the data processing apparatus 90 comprise: the RAM 82 to store data or utility softwares¹ the timer IC 81 which is used to calculate an execution period or an operation time¹ the key input controller 85 to convert a key input code from the key input section 86 into a code in a form which can be easily read by the CPU 65; the display controller 66 to convert display characters sent from the CPU 65 so as to be displayed by the display 68 such as LCD or plasma display; the KANJI (or Chinese characters) ROM 69 which is referred to as necessary to convert the display characters to the display 68 into KANJI¹ the I/O controller 71 of a communication control IC such as an RS232C or the like for communication with the outside; ~~the~~ a DMA controller 70 to control a DMA (direct memory access) from the I/O controller 71; and the like.--

Please amend the paragraph starting at page 10, line 25 as follows.

--Information (01 (hexadecimal notation) in the case where the device is an RAM card; 02 in the case of ROM card; F0 in the case of the pseudo IC card 1) indicative of the kind of IC card 50 or the ~~distinguishment~~ distinction between the IC card 50 and the pseudo IC card 1 is stored into address 0.--

Please amend the paragraph starting at page 11, line 11 as follows.

--In the case of the pseudo IC card 1, the head address of a device driver program to gain access to the pseudo IC card 1 stored in the ROM 83 on the apparatus main body side is stored into addresses 2 and 3. --